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PTO/SB/21 (04-04)

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| <b>TRANSMITTAL FORM</b><br>(to be used for all correspondence after initial filing) | Application Number     | 10/711,137       |
|   | Filing Date            | August 27, 2004  |
|   | First Named Inventor   | Crosland, Andrew |
|   | Art Unit               | Unassigned       |
|   | Examiner Name          | Unassigned       |
| Total Number of Pages in This Submission  | Attorney Docket Number | 015114-053510US  |

| ENCLOSURES (Check all that apply)  |   |  |
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| <input type="checkbox"/> Fee Transmittal Form<br><input type="checkbox"/> Fee Attached<br><input type="checkbox"/> Amendment/Reply<br><input type="checkbox"/> After Final<br><input type="checkbox"/> Affidavits/declaration(s)<br><input type="checkbox"/> Extension of Time Request<br><input type="checkbox"/> Express Abandonment Request<br><input checked="" type="checkbox"/> Information Disclosure Statement<br><input type="checkbox"/> Certified Copy of Priority Document(s)<br><input type="checkbox"/> Response to Missing Parts/Incomplete Application<br><input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53 | <input type="checkbox"/> Drawing(s)<br><input type="checkbox"/> Licensing-related Papers<br><input type="checkbox"/> Petition<br><input type="checkbox"/> Petition to Convert to a Provisional Application<br><input type="checkbox"/> Power of Attorney, Revocation<br>Change of Correspondence Address<br><input type="checkbox"/> Terminal Disclaimer<br><input type="checkbox"/> Request for Refund<br><input type="checkbox"/> CD, Number of CD(s) _____ | <input type="checkbox"/> After Allowance Communication to Technology Center (TC)<br><input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences<br><input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief)<br><input type="checkbox"/> Proprietary Information<br><input type="checkbox"/> Status Letter<br><input checked="" type="checkbox"/> Other Enclosure(s) (please identify below):<br>Return Postcard<br>PTO Form SB/08A and SB/08B |
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| SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT |   |
|--|---|
| Firm or Individual name                    | Townsend and Townsend and Crew LLP<br>Melvin D. Chan<br>Reg. No. 39,626 |
| Signature                                  |   |
| Date                                       | 10-27-04  |

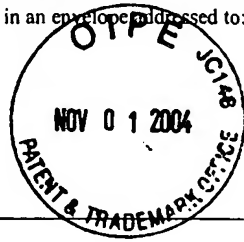
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TOWNSEND and TOWNSEND and CREW LLP

By:

  
Tiffany Wu

PATENT  
Attorney Docket No.: 15114-053510US  
Client Reference No.: A618

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:

ANDREW CROSLAND et al.

Application No.: 10/711,137

Filed: August 27, 2004

For: EMBEDDED PROCESSOR WITH  
WATCHDOG TIMER FOR  
PROGRAMMABLE LOGIC

Examiner: Unassigned

Art Unit: Unassigned

INFORMATION DISCLOSURE  
STATEMENT UNDER 37 CFR §1.97 and  
§1.98

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P.O. Box 1450  
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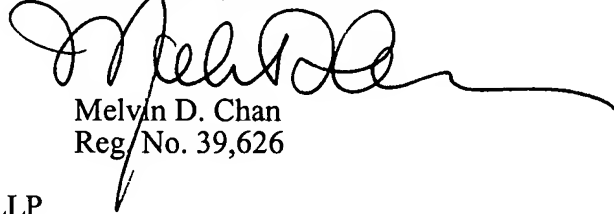
Sir:

The references cited on attached form PTO/SB/08A and PTO/SB/08B are being called to the attention of the Examiner. In accordance with 37 CFR §1.98(d), copies of the references can be found in Application No. 09/880,734, filed June 12, 2001 (Attorney Docket No. 015114-053500US). It is respectfully requested that the cited references be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

As provided for by 37 CFR 1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this statement and no representation is being made that a search has been conducted or that this statement encompasses all the possible relevant information.

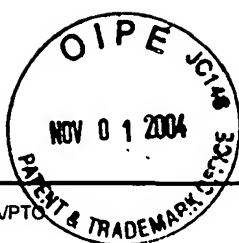
Applicant believes that no fee is required for submission of this statement. However, if a fee is required, the Commissioner is authorized to deduct such fee from the undersigned's Deposit Account No. 20-1430. Please deduct any additional fees from, or credit any overpayment to, the above-noted Deposit Account.

Respectfully submitted,



Melvin D. Chan  
Reg/No. 39,626

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PTO/SB/08A (08-03)

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|---|------------|----|--------------------------|------------------------|-----------------|
| <b>Substitute for form 1449A/PTO</b><br><br><b>INFORMATION DISCLOSURE<br/>STATEMENT BY APPLICANT</b><br><br>(use as many sheets as necessary) |            |    | <b>Complete if Known</b> |                        |                 |
|   |            |    | Application Number       | 10/711,137             |                 |
|   |            |    | Filing Date              | August 27, 2004        |                 |
|   |            |    | First Named Inventor     | Crosland, Andrew       |                 |
|   |            |    | Art Unit                 | Unassigned             |                 |
| Examiner Name   | Unassigned |    |                          |                        |                 |
| Sheet   | 1          | of | 3                        | Attorney Docket Number | 015114-053510US |

| U.S. PATENT DOCUMENTS+ |                          |  |                                |  |   |
|------------------------|--------------------------|--|--------------------------------|--|---|
| Examiner<br>Initials*  | Cite<br>No. <sup>1</sup> | Document Number                          | Publication Date<br>MM-DD-YYYY | Name of Patentee or<br>Applicant of Cited Document | Pages, Columns, Lines, Where<br>Relevant Passages or Relevant<br>Figures Appear |
|                        |                          | Number Kind Code <sup>2</sup> (if known) |                                |  |   |
|                        | AA                       | US-4,488,259                             | 12-11-1984                     | Mercy  |   |
|                        | AB                       | US-4,617,479                             | 10-14-1986                     | Hartmann et al.                                    |   |
|                        | AC                       | US-4,710,927                             | 12-01-1987                     | Miller   |   |
|                        | AD                       | US-4,871,930                             | 10-03-1989                     | Wong et al.  |   |
|                        | AE                       | US-5,241,224                             | 08-31-1993                     | Pedersen et al.                                    |   |
|                        | AF                       | US-5,258,668                             | 11-02-1993                     | Cliff et al.                                       |   |
|                        | AG                       | US-5,260,610                             | 11-09-1993                     | Pedersen et al.                                    |   |
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|                        | AJ                       | US-5,436,575                             | 07-25-1995                     | Pedersen et al.                                    |   |
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|                        | AL                       | US-5,790,479                             | 08-04-1998                     | Conn   |   |
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|                        | AN                       | US-6,233,205                             | 05-15-2001                     | Wells et al.                                       |   |
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| FOREIGN PATENT DOCUMENTS |                          |                           |                     |                                   |                                |   |  |                          |
|--------------------------|--------------------------|---------------------------|---------------------|-----------------------------------|--------------------------------|---|--|--------------------------|
| Examiner<br>Initials*    | Cite<br>No. <sup>1</sup> | Foreign Patent Document   |                     |                                   | Publication Date<br>MM-DD-YYYY | Name of Patentee or<br>Applicant of Cited<br>Document | Pages, Columns, Lines,<br>Where Relevant<br>Passages or Relevant<br>Figures Appear | T <sup>6</sup>           |
|                          |                          | Country Code <sup>3</sup> | Number <sup>4</sup> | Kind Code <sup>5</sup> (if known) |                                |   |  |                          |
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| Substitute for form 1449B/PTO<br><br><b>INFORMATION DISCLOSURE<br/>STATEMENT BY APPLICANT</b><br><br><i>(use as many sheets as necessary)</i> |   |    |   | <b>Complete if Known</b> |                  |
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|   |   |    |   | Filing Date              | August 27, 2004  |
|   |   |    |   | First Named Inventor     | Crosland, Andrew |
|   |   |    |   | Art Unit                 | Unassigned       |
|   |   |    |   | Examiner Name            | Unassigned       |
| Sheet   | 2 | of | 3 | Attorney Docket Number   | 015114-053510US  |

| NON PATENT LITERATURE DOCUMENTS |                       |   |                |
|---------------------------------|-----------------------|---|----------------|
| Examiner Initials *             | Cite No. <sup>1</sup> | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T <sup>2</sup> |
|                                 | AO                    | AITKEN, R.C., and AGARWAL, V.K., "A Diagnosis Method Using Pseudo-Random Vectors Without Intermediate Signatures," Proc. of Int. Conf. on Computer-Aided Design (ICCAD), IEEE pp. 574-577 (1989).   |                |
|                                 | AP                    | Altera "APEX 20K Programmable Logic Device Family Data Sheet," May 1999, 7 pages total.   |                |
|                                 | AQ                    | Altera "FLEX 10K Embedded Programmable Logic Family Data Sheet," May 1999, 7 pages total.   |                |
|                                 | AR                    | Altera "FLEX 8000 Programmable Logic Device Family Data Sheet," May 1999, 5 pages total.  |                |
|                                 | AS                    | Altera "IEEE 1149.1 (JTAG) Boundary-Scan Testing in Altera Devices," August 1999, Application Note 39, 29 pages total.  |                |
|                                 | AT                    | Altera "MAX 7000 Programmable Logic Device Family Data Sheet," May 1999, 6 pages total.   |                |
|                                 | AU                    | "AT94K Series Field Programmable System Level Integrated Circuit," Advance Information Brochure of Atmel Corporation, December 1999, 6 pages.   |                |
|                                 | AV                    | "CS2000 Reconfigurable Communications Processor Family Product Brief," Advance Product Information from ChameleonSystems, Inc., 2000, pages 1-8   |                |
|                                 | AW                    | DeHON, DPGA-Coupled Microprocessors: Commodity ICs for the Early 21st Century, Artificial Intelligence Laboratory, Massachusetts Institute of Technology, Cambridge, MA, IEEE, February, 1994, pp. 31-33  |                |
|                                 | AX                    | GHOSH-DASTIDAR, J., and TOUBA, N.A., "A Rapid and Scalable Diagnosis Scheme for BIST Environments With a Large Number of Scan Chains," Proc. of IEEE VLSI Test Symposium, pp. 79-85 (2000).   |                |
|                                 | AY                    | GHOSH-DASTIDAR, J., DAS, D., and TOUBA, N.A., "Fault Diagnosis in Scan-Based BIST using Both Time and Space Information," Proc. of International Test Conf., IEEE, pp. 95-102 (1999).   |                |
|                                 | AZ                    | HAUSER, and WAWRZYNEK, University of California, Berkeley, "Garp: A MIPS Processor with a Reconfigurable Coprocessor," IEEE, April 1997, pp. 12-21.   |                |
|                                 | BA                    | IBM Corporation, "Mixture of Field and Factory Programmed Logic Cells in a Single Device," IBM Technical Disclosure Bulletin, April 1995, pp. 499-500   |                |
|                                 | BB                    | MC ANNEY, M.G. and SAVIR, J., "There is Information in Faulty Signatures," Proc. of International Test Conf., IEEE, pp. 630-636 (1987).   |                |
|                                 | BC                    | "Motorola Technical Developments," Magazine of Motorola, Inc., Vol. 39, September 1999, pp. i-vii and 77-80.  |                |
|                                 | BD                    | NAGEL, "ACM Computing Surveys: Synergy Between Software and Hardware," Carnegie Mellon University, Department of Electrical Engineering and Computer Science, Pittsburgh, PA, December, 1996, pp. 1-3.  |                |

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| Examiner Signature |  | Date Considered |  |
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<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

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|   |   |    |   | <i>Examiner Name</i>          | Unassigned       |
| Sheet   | 3 | of | 3 | <i>Attorney Docket Number</i> | 015114-053510US  |

| NON PATENT LITERATURE DOCUMENTS |                          |   |                |
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| Examiner<br>Initials *          | Cite<br>No. <sup>1</sup> | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T <sup>2</sup> |
|                                 | BE                       | RAZDAN, and SMITH, "A High-Performance Microarchitecture with Hardware-Programmable Functional Units," Harvard University, Cambridge, MA, Digital Equipment Corporation, Hudson, MA, November, 1994, pp. 172-180.   |                |
|                                 | BF                       | "Triscend E5 Configurable System-on-Chip Family," Product Description from Triscend Corporation, January, 2000 (Version 1.00), pp. i-ii and 1-90.   |                |
|                                 | BG                       | "Wireless Base Station Design Using Reconfigurable Communications Processors," Wireless Base Station White paper from ChameleonSystems, Inc., 2000, pages 1-8.  |                |
|                                 | BH                       | WITTIG, and CHOW, "OneChip" An FPGA Processor With Reconfigurable Logic," Department of Electrical and Computer Engineering, University of Toronto, Ontario, Canada, IEEE, September, 1996, pp. 126-135.  |                |
|                                 | BI                       | WYNN, "In-Circuit Emulation for ASIC-Based Designs," Xilinx, Inc., VLSI Systems Design, October, 1986, pp. 38-39, and 42-45.  |                |
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